



Preliminary

# GRF5504

High Efficiency, 3.5 Watt PA  
Tuning Range: 0.4 to 0.5 GHz



## Product Description

GRF5504 is a high efficiency PA that delivers up to 3.5 Watts at Psat with Vcc at 5.0 volts and a low Iccq of 120 mA. PAE at Psat is roughly 64%.

The device can be tuned over a range of frequencies from around 400 MHz to 500 MHz with typical fractional bandwidths of 3 to 5%.

For frequencies in the 700-1000 MHz range, GRF5509 offers slightly higher output power and high efficiency with the same package and pinouts.

Consult with the GRF applications engineering team for custom tuning/evaluation board data.

## Features

Reference: 5.0V/460MHz/CW

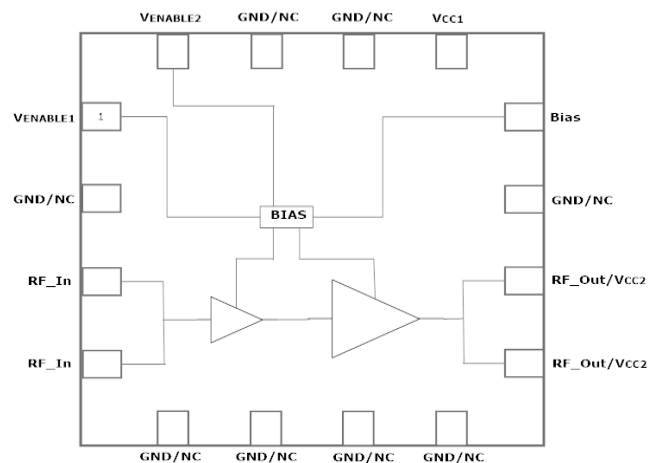
- Gain: 41.0 dB
- Iccq: 120 mA
- OP1dB: 34.0 dBm
- Psat: 35.5 dBm
- PAE at Psat: 64%

Reference: 3.5V/460MHz/CW

- Gain: TBD
- Iccq: TBD
- OP1dB: TBD
- Psat: TBD
- PAE: TBD
- Flexible Bias Voltage and Current
- Process: InGaP HBT

## Applications

- UHF
- Automatic Meter Reader
- RFID



3.0 x 3.0 mm QFN-16



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## Absolute Ratings:

Parameter	Symbol	Min.	Max.	Unit
Drain Voltage	V <sub>CC</sub>		5.5	V
Transient Average RF Input Power: (Load VSWR < 2:1; Duration: <1 hour)	P <sub>IN MAX</sub>		TBD	dBm
Operating Temperature (Package Heat Sink)	T <sub>AMB</sub>	-40	85	°C
Maximum Junction Temperature (MTTF > 10 <sup>6</sup> Hours)	T <sub>MAX</sub>		170	°C
Maximum Dissipated Power	P <sub>DISS MAX</sub>		1.0	W
Ruggedness: Vcc: 5.0 volt at Psat ( all phase angles)	VSWR	8:1		—
Electrostatic Discharge:				
Charged Device Model: (TBD)	CDM	1500		V
Human Body Model: (TBD)	HBM	250		V
Storage:				
Storage Temperature	T <sub>STG</sub>	-65	150	°C
Moisture Sensitivity Level	MSL		(TBD)	—



Caution! ESD Sensitive Device

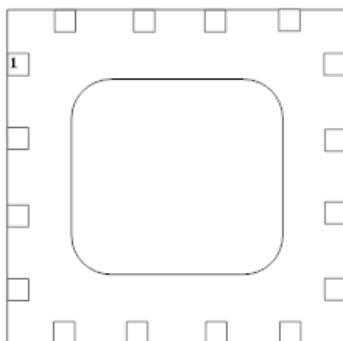


Exceeding Absolute Maximum Rating conditions may cause permanent damage to the device.

**Note: For package dimensions and manufacturing information, see the [Guerrilla-RF.com](http://Guerrilla-RF.com) website for the following document located on the GRF5504 landing page: Manufacturing Note—MN-001 Product Tape and Reel, Solderability and Package Outline Specification.**

[Link to manufacturing note](#)

## Pin Out (Top View)



## Pin Assignments:

Pin	Name	Description	Note
1	VENABLE1	Enable1 Voltage Input	VENABLE1 and series resistor set IccQ for the input stage. VENABLE < =0.2 volts disables device. On-die pull-down resistor will turn the part off if this node is allowed to float.
2	GND/NC	No Connect or Ground	No internal connection to die
3	RF_In	RF Input	Pins 3-4 tied together on system board
4	RF_In	RF Input	Pins 3-4 tied together on system board
5	GND/NC	Ground or No Connect	No internal connection to die
6	GND/NC	Ground or No Connect	No internal connection to die
7	GND/NC	Ground or No Connect	No internal connection to die
8	GND/NC	Ground or No Connect	No internal connection to die
9	RF_Out/Vcc2	PA Output/Bias Voltage	Pins 9-10 tied together on system board. Supply Vcc2 here.
10	RF_Out/Vcc2	PA Output/Bias	Pins 9-10 tied together on system board. Supply Vcc2 here.
11	GND/NC	Ground or No Connect	No internal connection to die
12	Bias	Bias Circuit Supply	Connect to Vcc2 through external resistor
13	Vcc1	Bias Voltage	Supply Vcc1 here.
14	GND/NC	Ground or No Connect	No internal connection to die
15	GND/NC	Ground or No Connect	No internal connection to die
16	VENABLE2	Enable2 Voltage Input	VENABLE2 and series resistor set IccQ for the output stage. VENABLE < =0.2 volts disables device. On-die pull-down resistor will turn the part off if this node is allowed to float.
PKG BASE	GND	Ground	Provides DC and RF ground for LNA, as well as thermal heat sink. Recommend multiple 8 mil vias beneath the package for optimal RF and thermal performance. Refer to evaluation board top layer graphic on schematic page.



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## Nominal Operating Parameters:

Parameter	Symbol	Specification			Unit	Condition
		Min.	Typ.	Max.		
<b>Performance:</b> (450 to 470 MHz Tune @25C)						Bias: 5.0 V /120 mA I <sub>ccq</sub> unless otherwise noted.
Test Frequency	F <sub>TEST</sub>		460		MHz	
Gain (Small Signal)	S(2,1)		41.0		dB	
Output 1dB Compression Power	OP1dB		34.0		dBm	
Saturated Output Power	Psat		35.5		dBm	
Power Added Efficiency (at Psat)	PAE		64		%	
Switching Rise Time	T <sub>RISE</sub>		TBD		ns	
Switching Fall Time	T <sub>FALL</sub>		TBD		ns	
Quiescent Supply Current	I <sub>DDQ</sub>		120		mA	V <sub>CC</sub> = Venable1/2 = 5.0 volts
Enable Current 1	I <sub>ENABLE1</sub>		1.2		mA	
Enable Current 2	I <sub>ENABLE2</sub>		TBD		mA	
<b>Disabled Mode</b>						
Supply Current (Leakage)	I <sub>DD</sub>		1.0		uA	
<b>Thermal Data</b>						
Thermal Resistance: (IR Scan Method)	Θ <sub>JC</sub>		TBD		°C/W	
Junction Temperature @ +85C Reference (package heat sink)	T <sub>CHANNEL</sub>		TBD		°C	V <sub>DD</sub> : 5.0 volts; I <sub>CC</sub> : TBD P <sub>DISS</sub> : TBD; RF: TBD

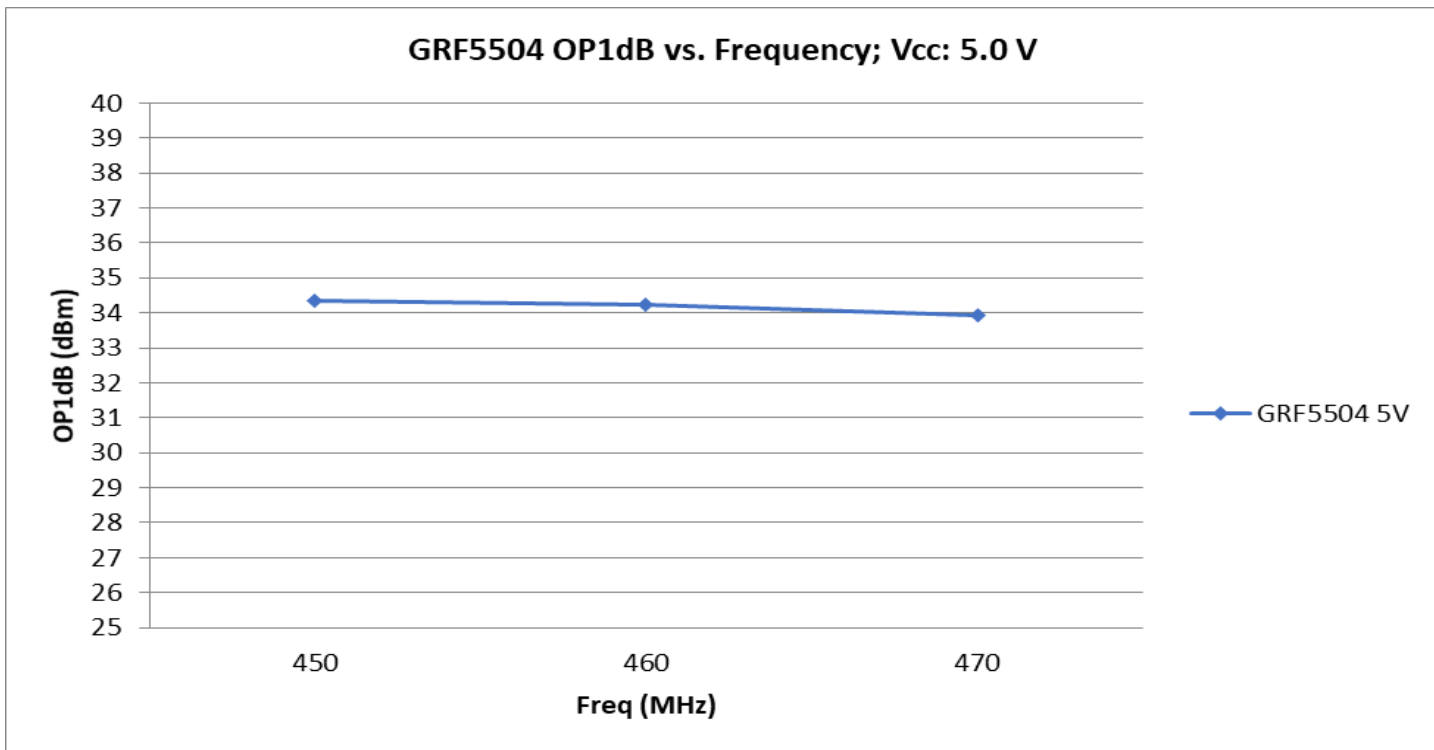
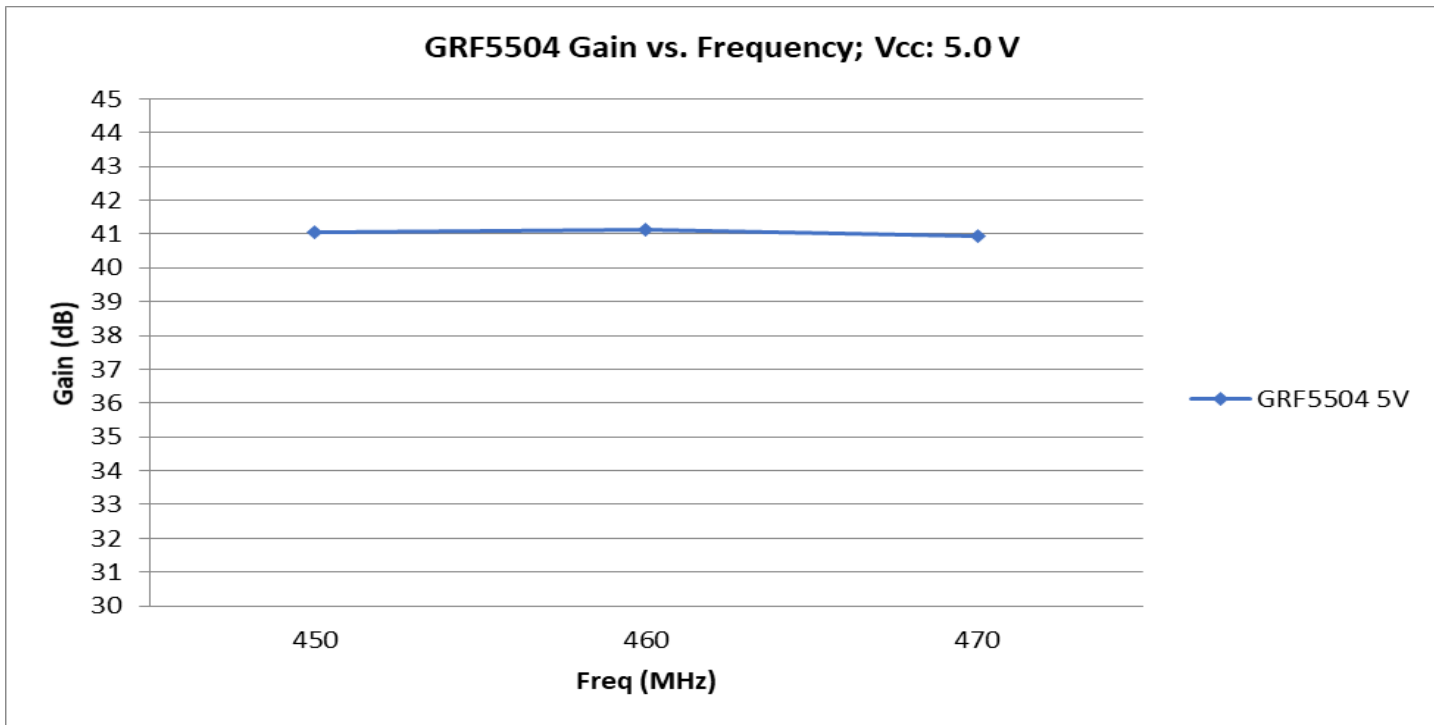


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## GRF5504 Evaluation Board Measured Data; Vcc: 5.0 volts



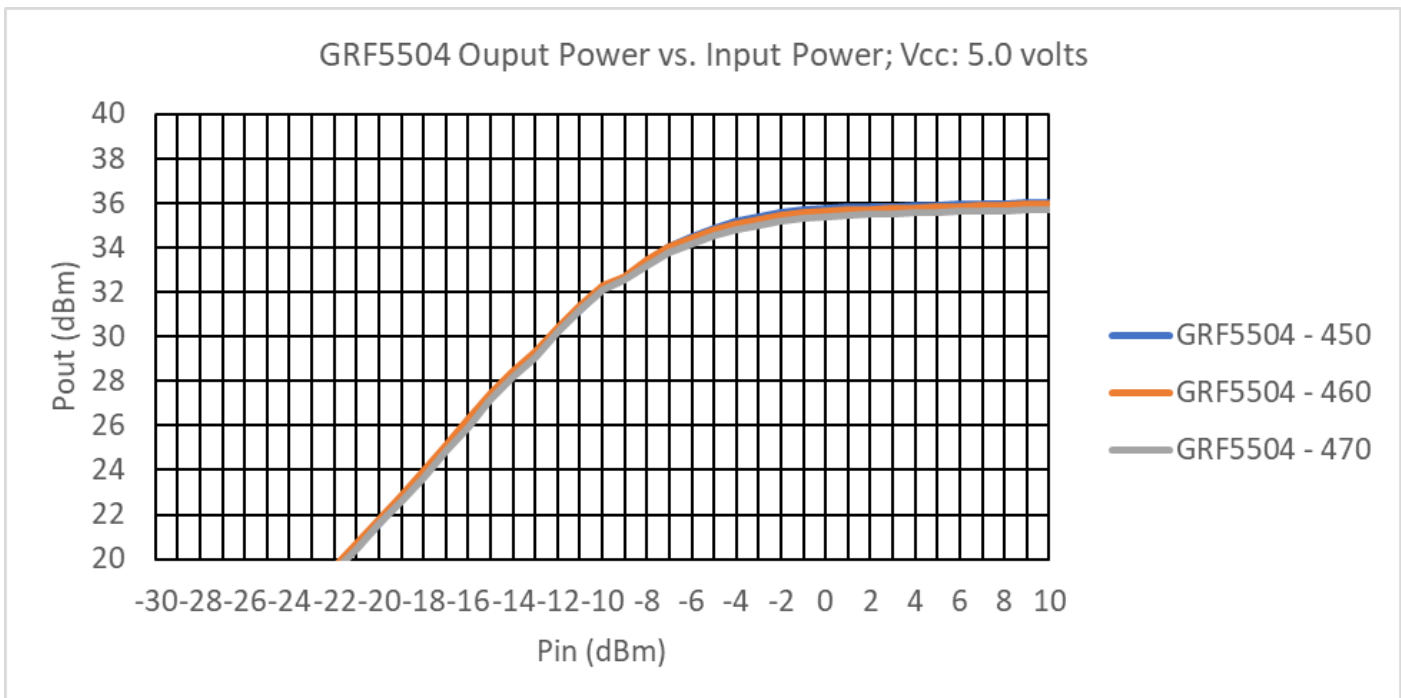
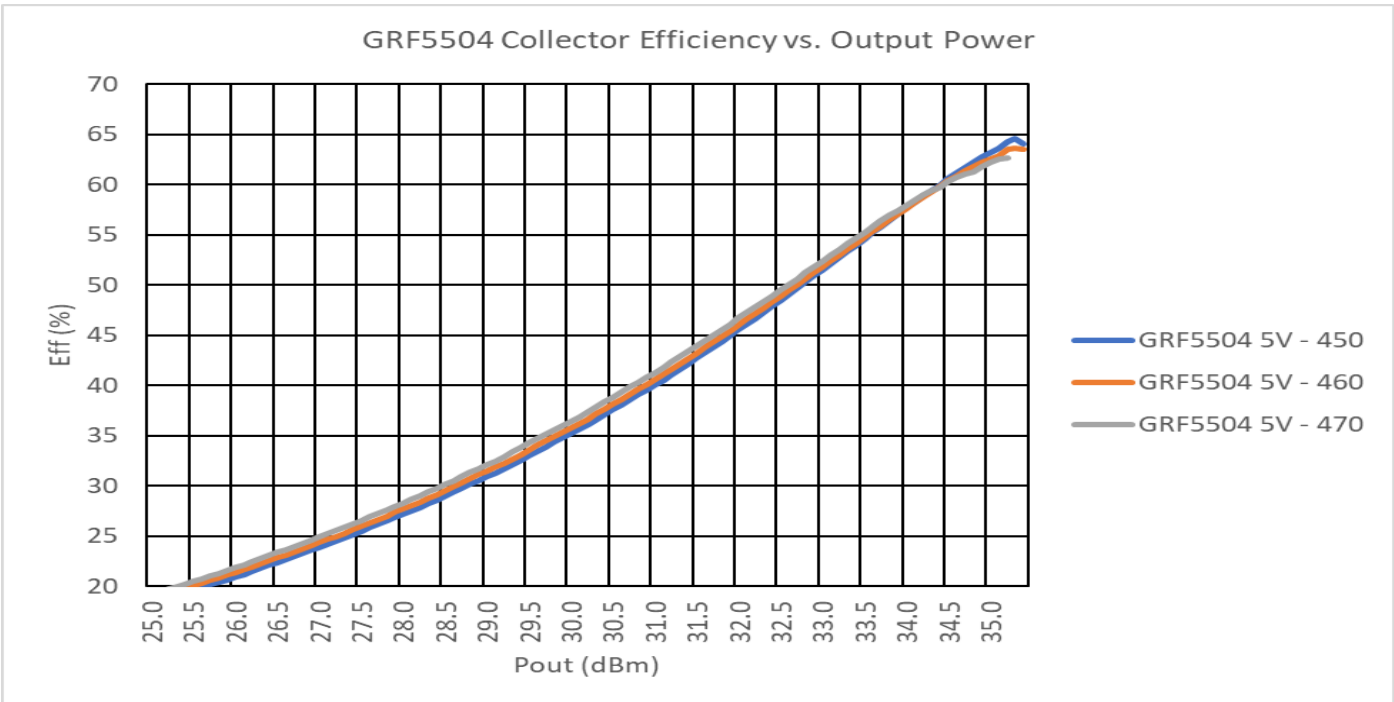


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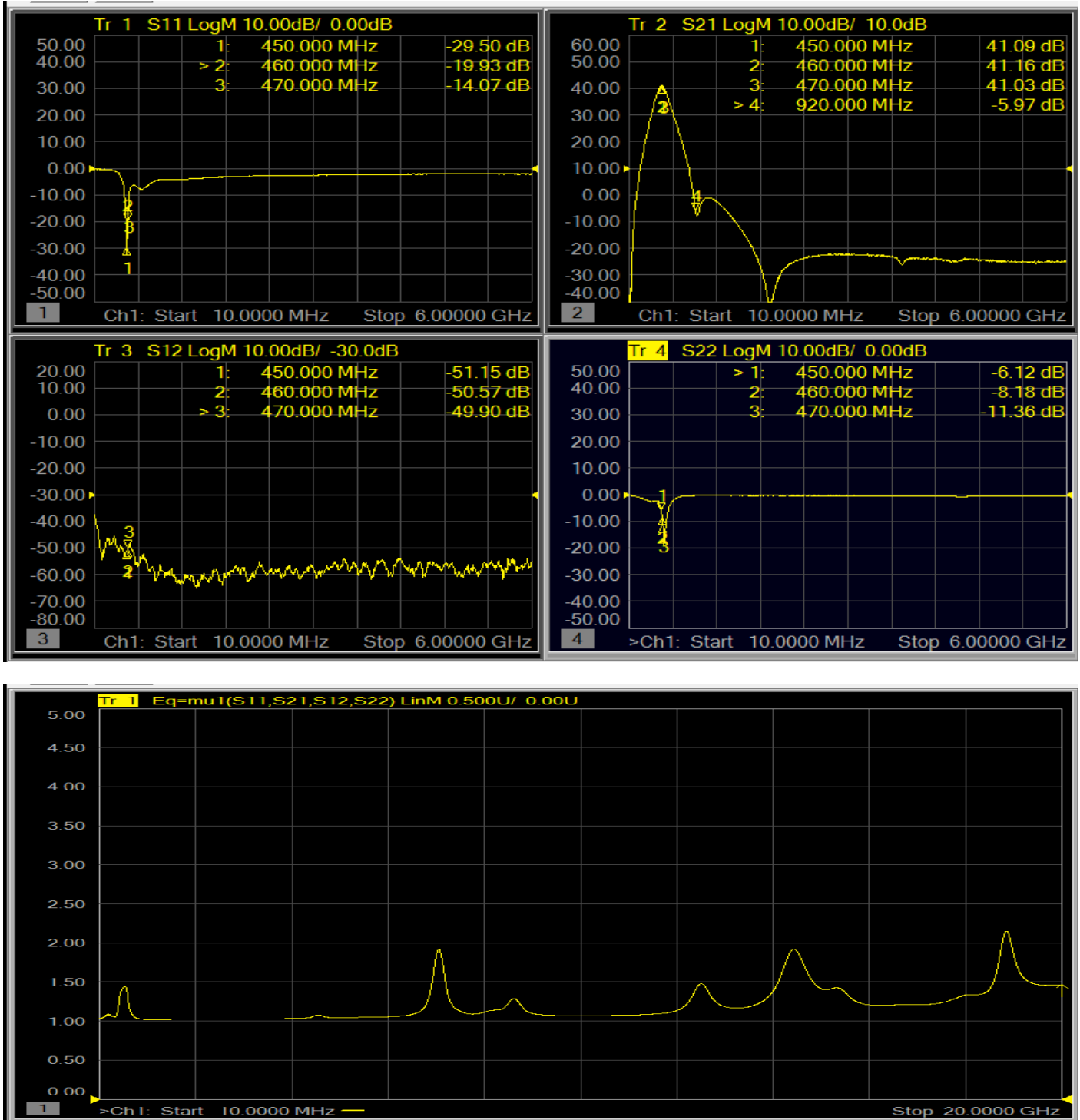


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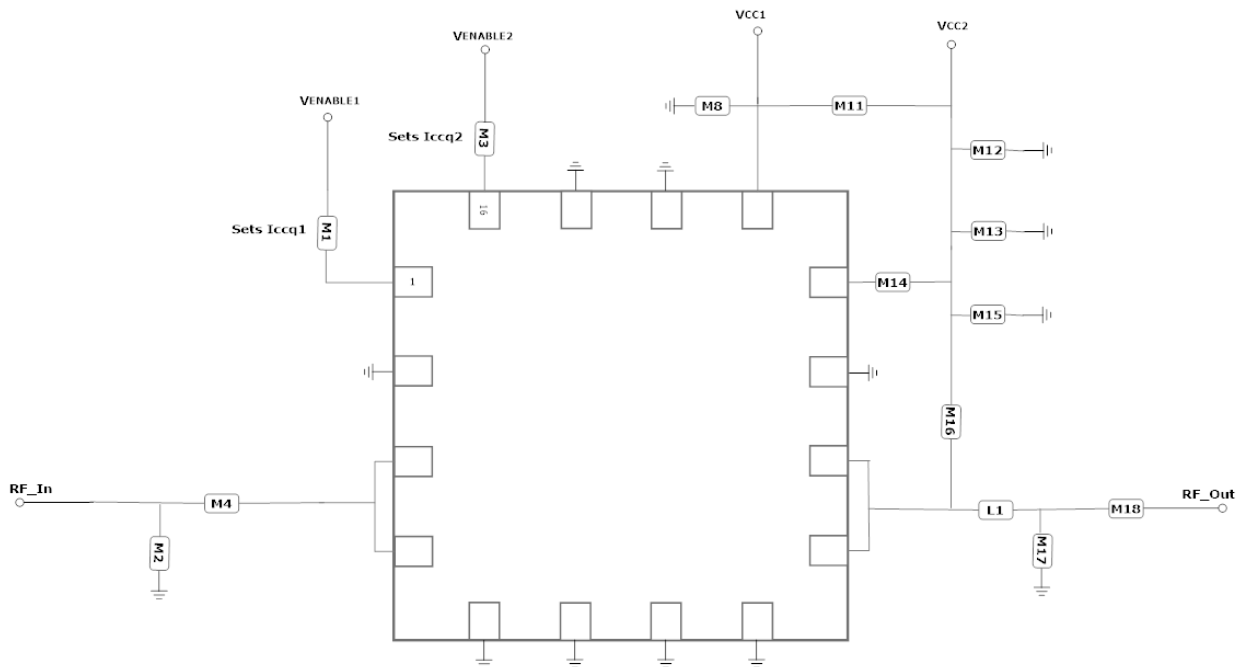
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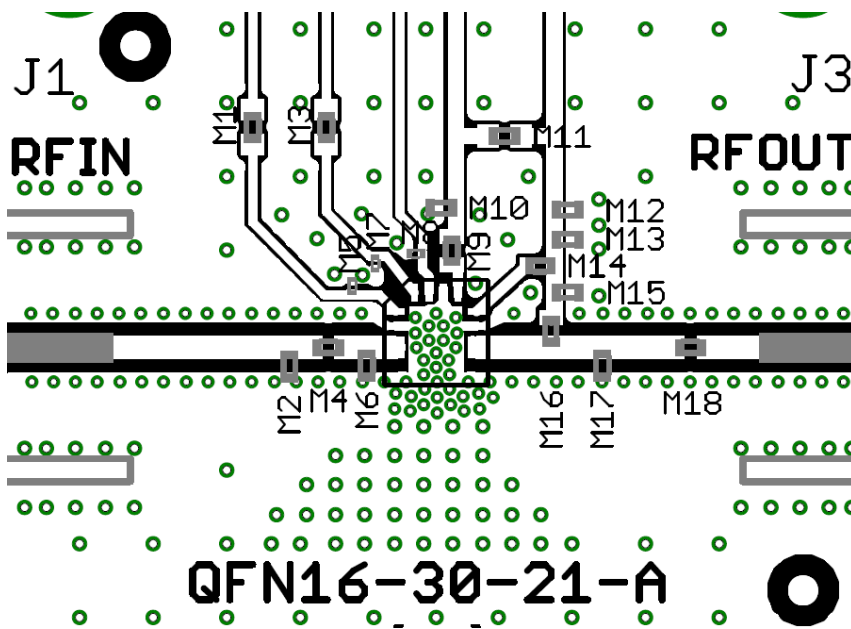
## GRF5504 Evaluation Board Small Signal S-Pars: (450 to 470 MHz Tune)



Note: Mu factor  $\geq 1.0$  implies unconditional stability



GRF5504 Application Schematic: (450 to 470 MHz)



GRF5504 Evaluation Board Assembly Drawing





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## GRF5504 Evaluation Board BOM: (450 to 470 MHz tune)

Vcc= Venable1= Venable2 = 5.0 volts

Component	Type	Manufacturer	Family	Value	Package Size	Substitution
M1	Resistor	Various	5%	2.4k Ohm	0402	ok
M2	Inductor	Murata	LQG	6.8 nH	0402	ok
M3	Resistor	Various	5%	5.4k Ohm	0402	ok
M4	Capacitor	Murata	GJM	27 pF	0402	ok
M9	Inductor	Murata	LQW	5.6 nH	0402	ok
M10	Capacitor	Murata	GRM	0.1 uF	0402	ok
M11	Resistor (Jumper)	Various	5%	0 Ohm	0402	ok
M12	Capacitor	Murata	GRM	0.1 uF	0402	ok
M13	Capacitor	Murata	GRM	100 pF	0402	ok
M14	Resistor (Jumper)	Various	5%	0 Ohm	0402	ok
M16	Inductor: High Q	Coilcraft	Micro Spring	23 nH	—	ok
L1 (near M17)	Inductor	Coilcraft	HP	3.3 nH	0402	ok
M17	Capacitor	Murata	GJM	20 pF	0402	ok
M18	Capacitor	Murata	GJM	15 pF	0402	ok
Evaluation Board	QFN16-30-21-A					

**GRF5504 Bias Resistor (M1) Selection Curves (TBD)**

**GRF5504 Bias Resistor (M2) Selection Curves (TBD)**



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Data Sheet Release Status:	Notes
Advance	S-parameter and NF data based on EM simulations for the fully packaged device using foundry supplied transistor s-parameters. Linearity estimates based on device size, bias condition and experience with related devices.
Preliminary	All data based on evaluation board measurements in the Guerrilla RF Applications Lab.
Released	All data based on device qualification data. Typically, this data is nearly identical to the data found in the preliminary version. Max and min values for key RF parameters are included.

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